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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
10/786,097	02/26/2004	Masahiro Kawasaki	500.43534X00	2499		
20457	7590 06/06/2005		EXAM	EXAMINER		
	LI, TERRY, STOUT & I	PRENTY,	PRENTY, MARK V			
1300 NOR1. SUITE 1800	H SEVENTEENTH STREE	ART UNIT	PAPER NUMBER			
ARLINGTON, VA 22209-3873			2822	2822		
			DATE MAILED: 06/06/2005			

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Application	on No.	Applicant(s)				
Office Action Summers		10/786,09	7	KAWASAKI ET AL.				
Onice	Action Summary	Examiner	<u>-</u>	Art Unit				
·		MARK V. I		2822				
The MAILI Period for Reply	NG DATE of this communication	on appears on the	cover sheet with the c	orrespondence ad	dress			
THE MAILING DA  - Extensions of time marger SIX (6) MONTHS  - If the period for reply of the period for reply in Failure to reply within Any reply received by	STATUTORY PERIOD FOR INTERIOR OF THIS COMMUNICAT by be available under the provisions of 37 of the mailing date of this communication of the pecified above is less than thirty (30) days a specified above, the maximum statutory he set or extended period for reply will, by the Office later than three months after the justment. See 37 CFR 1.704(b).	TION. CFR 1.136(a). In no evention. s, a reply within the stature period will apply and will y statute, cause the apply	nt, however, may a reply be tim tory minimum of thirty (30) days I expire SIX (6) MONTHS from ication to become ABANDONEI	nely filed s will be considered timel the mailing date of this co D (35 U.S.C. § 133).				
Status								
1) Responsive	to communication(s) filed or	n 26 February 200	)4.					
2a) This action		This action is no						
•	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claim	s ·							
4a) Of the a 5)⊠ Claim(s) <u>1-</u> 6)⊠ Claim(s) <u>17</u> 7)⊠ Claim(s) <u>21</u>	26 is/are pending in the applications bove claim(s) is/are with a sign of the application is/are with and 22-26 is/are allowed.  is/are objected to.  are subject to restriction	ithdrawn from cor						
Application Papers								
10)⊠ The drawing Applicant ma Replacemen	ation is objected to by the Ext(s) filed on 26 February 2004 by not request that any objection that drawing sheet(s) including the declaration is objected to by	4 is/are: a)⊠ acc to the drawing(s) b correction is require	e held in abeyance. See ed if the drawing(s) is obj	e 37 CFR 1.85(a). ected to. See 37 CF	FR 1.121(d).			
Priority under 35 U.S	S.C. § 119							
12)⊠ Acknowledg a)⊠ All b)□ 1.⊠ Certii 2.□ Certii 3.□ Copie	ment is made of a claim for for Some * c) None of:  The copies of the priority doctoried copies of the priority doctories of the certified copies of the cation from the International Exhed detailed Office action for	uments have bee uments have bee e priority docume Bureau (PCT Rule	n received. n received in Applicati ents have been receive e 17.2(a)).	on No ed in this National	Stage			
Attachment(s)  1)  Notice of Reference	s Cited (PTO-892)		4) Interview Summary	(PTO-413)				
2) Notice of Draftspers 3) Information Disclosu	on's Patent Drawing Review (PTO-9 re Statement(s) (PTO-1449 or PTO/ te <u>February 26, 2004</u> .		Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	ate	O-152)			

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This Office Action is in response to the papers filed on February 26, 2004.

Claims 17-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over United States Patent 6,569,707 to Dimitrakopoulos et al. (Dimitrakopoulos) together with United States Patent 6,858,527 to Gracias.

With respect to independent claim 17, Dimitrakopoulos discloses a semiconductor device (see the entire patent, including the Fig. 1 disclosure, for example) using a thin-film transistor obtained by laminating a gate electrode 14, a gate insulator 12, a source electrode 16, a drain electrode 16 and a semiconductor layer 20 in that order on an insulating substrate 10, wherein an organic compound layer 18 different from said semiconductor layer is present between each of said source and drain electrodes and said semiconductor layer.

The difference between claim 17 and Dimitrakopoulos is claim 17 further recites: "and the average thickness of the organic compound layer is not more than 10 Å and not less than 1 Å" (Dimitrakopoulos does not disclose the thickness of its organic compound layer 18, which is a self-assembled monolayer comprising a thiol compound (see the paragraph bridging columns 5 and 6, for example)).

Gracias teaches that a self-assembled monolayer comprising a thiol compound has a thickness of approximately 10 Å to 20 Å (see the entire patent, including the Abstract, for example).

It would have been obvious to one skilled in this art that Dimitrakopoulos's organic compound layer 18, which is a self-assembled monolayer comprising a thiol

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compound, is approximately 10 Å thick, because Gracias teaches that a self-assembled monolayer comprising a thiol compound is approximately 10 Å thick.

Claim 17 is thus rejected under 35 U.S.C. 103(a) as being unpatentable over Dimitrakopoulos together with Gracias.

With respect to dependent claim 18, the organic compound layer 18 present between each of Dimitrakopoulos's source and drain electrodes 16 and semiconductor layer 20 has a thiol group (see the paragraph bridging columns 5 and 6).

Claim 18 is thus rejected under 35 U.S.C. 103(a) as being unpatentable over Dimitrakopoulos together with Gracias.

With respect to dependent claim 19, the organic compound layer 18 present between each of Dimitrakopoulos's source and drain electrodes 16 and semiconductor layer 20 is an alkane thiol (see the paragraph bridging columns 5 and 6).

Claim 19 is thus rejected under 35 U.S.C. 103(a) as being unpatentable over Dimitrakopoulos together with Gracias.

With respect to dependent claim 20, Dimitrakopoulos's source and drain electrodes 16 are made of gold, silver, copper, platinum, palladium, tin, or an alloy or mixture containing two or more of these metals (see column 5, lines 29-38).

Claim 20 is thus rejected under 35 U.S.C. 103(a) as being unpatentable over Dimitrakopoulos together with Gracias.

Claim 21 is objected to as being dependent upon a rejected base claim, but would be allowable over the prior art of record if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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Claims 1-16 and 22-26 are allowable over the prior art of record.

The prior art of record does not disclose or suggest the allowable thin-film transistors taken as a whole, including the semiconductor layers.

United States Patent Application Publication 2005/0056897 is related to this application.

Registered practitioners can telephone the examiner at (571) 272-1843. Any voicemail message left for the examiner must include the name and registration number of the registered practitioner calling, and the Application/Control (Serial) Number. Technology Center 2800's general telephone number is (571) 272-2800.

Mark V. Prenty Primary Examiner

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